

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Teiichiro NISHIZAKA, et al.

Serial No.: Not Yet Assigned Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith Examiner: Unknown

For: SEMICONDUCTOR MEMORY DEVICE AND CONTROL METHOD AND
MANUFACTURING METHOD THEREOF

Honorable Commissioner of Patents
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner. In compliance with the concise explanation requirement under 37 CFR §1.98(a)(3), the relevance of these documents is discussed on pages 1, 2, 7 and 9 of the subject application. Further, English-language Abstracts are attached to the references.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,



Sean M. McGinn
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Date: 8/27/03

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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

NEG-302US

Application Number

Not Yet Assigned

Applicant(s)

Teiichiro NISHIZAKA, et al.

Filing Date

Concurrently Herewith

Group Art Unit

Not Yet Assigned

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		6,256,231 B1	07/03/2001	Lavi et al			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		5-174583	07/13/1993	Japan			ABS	
		5-343645	12/24/1993	Japan			ABS	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		Chan, et al., "A True Single-Transistor Oxide-Nitride-Oxide EEPROM Device", IEEE Vol. EDL-8, No. 3, 1987, p. 93

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.